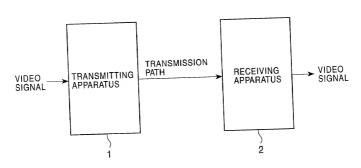
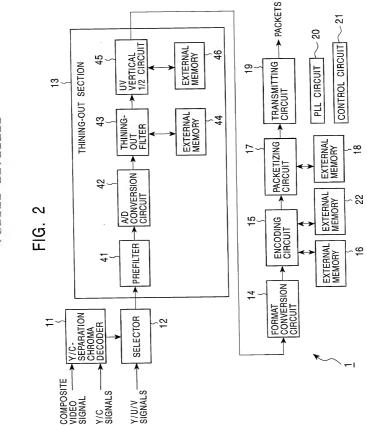
FIG. 1



1191

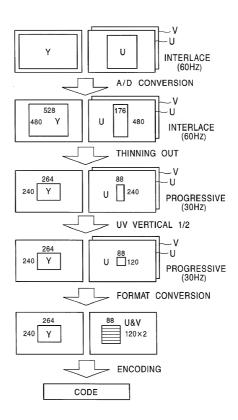
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FIG. 3





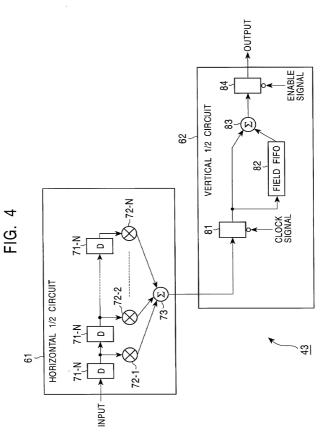
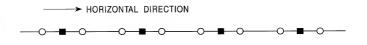


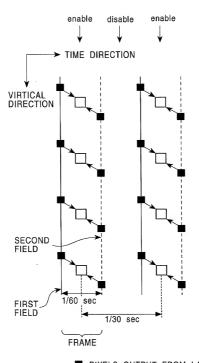
FIG. 5



O PIXELS INPUT TO HORIZONTAL 1/2 CIRCUIT

■ PIXELS OUTPUT FROM LATCH CIRCUIT 81

FIG. 6



- PIXELS OUTPUT FROM LATCH CIRCUIT 81
- PIXELS OUTPUT FROM LATCH CIRCUIT 84

FIG. 7

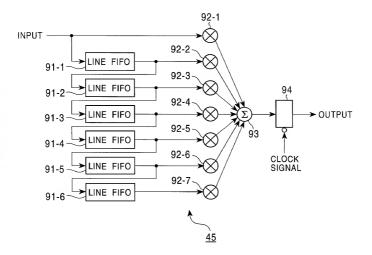
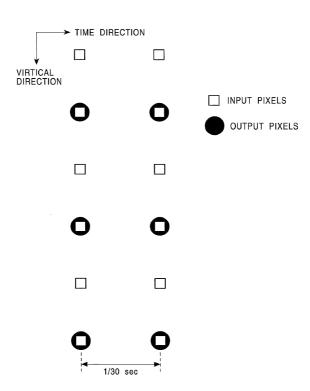


FIG. 8





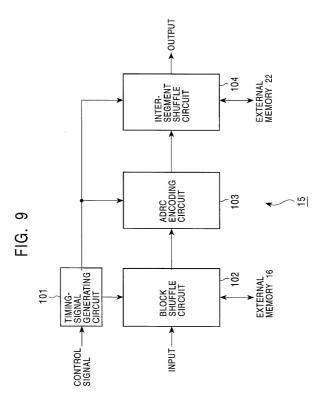


FIG. 10

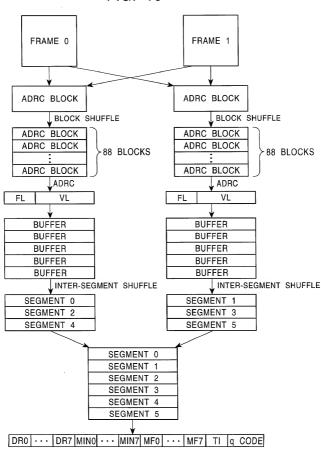


FIG. 1:

